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APPLICATION NO.	F	LING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/629,940	1	07/29/2003	Charles Hartman	200310736-1	9039
22879	7590	05/19/2006	EXAM	EXAMINER	
		RD COMPANY	DALEY, CHRISTO	DALEY, CHRISTOPHER ANTHONY	
		4 E. HARMONY RO OPERTY ADMINIS	ART UNIT	PAPER NUMBER	
FORT COLLINS, CO 80527-2400				2111	
				DATE MAILED: 05/19/2006	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)			
	10/629,940	HARTMAN ET AL.			
Office Action Summary	Examiner	Art Unit			
	Christopher A. Daley	2111			
The MAILING DATE of this communication app Period for Reply	ears on the cover sheet with the c	orrespondence address			
A SHORTENED STATUTORY PERIOD FOR REPLY WHICHEVER IS LONGER, FROM THE MAILING DA - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period v - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUNICATION 36(a). In no event, however, may a reply be tim vill apply and will expire SIX (6) MONTHS from , cause the application to become ABANDONE	lely filed the mailing date of this communication. (35 U.S.C. § 133).			
Status					
1) ⊠ Responsive to communication(s) filed on <u>03 Ay</u> 2a) ☐ This action is FINAL. 2b) ⊠ This 3) ☐ Since this application is in condition for allowar closed in accordance with the practice under E	action is non-final. nce except for formal matters, pro				
Disposition of Claims					
4) ⊠ Claim(s) <u>1-9</u> is/are pending in the application. 4a) Of the above claim(s) is/are withdray 5) □ Claim(s) is/are allowed. 6) ⊠ Claim(s) <u>1-9</u> is/are rejected. 7) □ Claim(s) is/are objected to. 8) □ Claim(s) are subject to restriction and/or					
Application Papers					
9) The specification is objected to by the Examine 10) The drawing(s) filed on is/are: a) accomplicant may not request that any objection to the Replacement drawing sheet(s) including the correct 11) The oath or declaration is objected to by the Example 11.	epted or b) objected to by the Eddrawing(s) be held in abeyance. See ion is required if the drawing(s) is obj	e 37 CFR 1.85(a). ected to. See 37 CFR 1.121(d).			
Priority under 35 U.S.C. § 119					
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 					
Attachment(s) 1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date	4) Interview Summary Paper No(s)/Mail Da 5) Notice of Informal P 6) Other:				

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DETAILED ACTION

1. Claims 1 – 9 are pending.

Response to Arguments

2. Applicant's arguments, filed April 03, 2006, with respect to the rejection(s) of claim(s) 1 under Jeddeloh have been fully considered and are persuasive. Therefore, the rejection has been withdrawn. However, upon further consideration, a new ground(s) of rejection is made in view of Ajanovic.

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 4. Claims 1 4, and 9 are rejected under 35 U.S.C. 102(b) as being anticipated by Ajanovic et al (US5859988) hereinafter Ajanovic.
- 5. As to claim 1, Ajanovic discloses a configurable I/O bus architecture, comprising: a system bus interface device (Figure 3 illustrates a system bus interface device, 301); first and second I/O bus interface devices (port B interface 302, and port interface 303);

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first and second intermediate buses (first and second intermediate busses that are outputs of data buffers 304, and 306 respectively);

a switching device (figure 3, arbitration and control unit 308); and a steering signal; wherein: the first intermediate bus couples the system bus interface device to the first I/O bus interface device (Arbitration and control unit 308 produces a signal that determined the coupling of interface unit 301 to 302, COL. 5, line 65 – COL. 6, line 3);

the second intermediate bus couples the system bus interface device to the switching device (Said bus is coupled to switch 308, COL. 7, lines 38 - 46); and the switching device is operable to couple the second intermediate bus either to the first or to the second I/O bus interface device responsive to the steering signal (Switching device 308 arbitrates between port B and Port C, dependent on the control signal, COL. 7, lines 38 - 43).

6. As to claim 2, Ajanovic discloses the configurable I/O bus architecture: further comprising at least a first signal indicating whether an I/O device is coupled to the second I/O bus interface device (Signal 206 is said signal, COL. 7, lines 40 - 45); and wherein the steering signal is derived from the first signal such that the steering signal assumes a first state when the I/O device is so coupled and a second state when the I/O device is not so coupled (The steering signal arbitrates the enablement of either interface 302 or 303, COL. 7, lines 10 - 25).

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7. As to claim 3, Ajanovic discloses the configurable I/O bus architecture, wherein: the switching device couples the second intermediate bus to the second I/O bus interface device when the steering signal assumes the first state, and couples the second intermediate bus to the first I/O bus interface device when the steering signal assumes the second state (The steering signal arbitrates the enablement of either interface 302 or 303, COL. 7, lines 10 - 25).

As to claim 4, Ajanovic discloses the configurable I/O bus architecture: further comprising a second signal indicating whether the I/O device is coupled to the second I/O bus interface device (Figure 3 illustrates control signal from arbiter 308 to second interface 303); and wherein the steering signal is derived from both the first and second signals using a logic gate (It is well known in the art that the inputs of controlled elements are inputs into the arbitration logic).

8. As to claim 9, Ajanovic discloses the configurable I/O bus architecture: wherein the first and second intermediate buses are rope buses (Figure 2 illustrates an embodiment where a rope configuration is present, Col. 3, lines 28 - 63).

Claim Rejections - 35 USC § 103

- 9. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the

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invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

- 10. Claims 5 8 are rejected under 35 U.S.C. 103(a) as being unpatentable over Ajanovic in view of Alexender et al (US6510529), hereinafter Alexender.
- 11. As to claim 5,7 8, Ajanovic does not disclose a hand-operated switch for an I/O bus;

However, Alexender teaches of a hand-operated switch 104 controlled by a panel button that will enable/disable the coupling of PCI bridge 108 to system controller 102. It would have been obvious to one of ordinary skill in the art at the time of the invention to incorporate the teaching of Alexender into Ajanovic as Alexender's teaching provides a human safety over-ride, when a fault condition arises COL. 5, lines 43 – 67. One of ordinary skill in the art would have been motivated to use the bridge of Ajanovic in the system of Alexender to provide a manual safety override, when a fault occurs, COL. 5, lines 43 – 67.

12. As to claim 6, Ajanovic discloses the configurable I/O bus architecture, wherein: the switching device couples the second intermediate bus to the second I/O bus interface device when the steering signal assumes the first state, and couples the second intermediate bus to the first I/O bus interface device when the steering signal assumes the second state ((The steering signal arbitrates the enablement of either interface 302 or 303, COL. 7, lines 10 - 25).

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Conclusion

13. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Christopher A. Daley whose telephone number is 571 272 3625. The examiner can normally be reached on 9 am. - 4p m.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mark Rinehart can be reached on 571 272 3632. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

CAD 5/12/2006 MARK H. RIMEHART SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2100